Abstracts

Novel Architecture and MMICs for an Integrated Front End of a Spectrum Analyzer (1995 Vol. II [MWSYM])

T. Takenaka, A. Miyazaki and H. Matsuura. "Novel Architecture and MMICs for an Integrated Front End of a Spectrum Analyzer (1995 Vol. II [MWSYM])." 1995 MTT-S International Microwave Symposium Digest 95.2 (1995 Vol. II [MWSYM]): 737-740.

This paper proposes a novel architecture and MMICs for an integrated front end of a 2-32 GHz spectrum analyzer. The architecture achieves miniaturization by eliminating the YIG tracking filter. The MMICs achieve ultra-wideband performances as well as chip-size reduction by utilizing novel FET cells for the basic circuit functions.

❑ Return to main document.